FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. ARC.015RC1	APPLICATION NO. 09/808,469
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT Peter Warnes	
	FILING DATE March 14, 2001	GROUP 2122 2191

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
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MX	3	5,544,067	8/6/96	Rostoker, et al.			
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MX	14	2 308 470	6/97	GB				
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MX	16	Elms, A., "Tuning a Customisable RISC Core for DSP," Electronic Product Design, Sept. 1997, Vol. 18, No. 9, pages 19-20, 22, XP000909039
MX	17	Tensilica, Inc; Xtensa Instruction Set Architecture (ISA) Reference Manual Revision 1.0. (First 25 pages)
MX	18	Tensilica, Inc. Instruction Extension Language (TIE) Reference Manual Revision 1.3.
MX	19	Compton et al., "Configurable Computing: A Survey of Systems and Software," Technical Report, Northwestern University, Dept. of ECE, 1999.
MX	20	Yang, Jin-Hyuk, et al., "MetaCore: A Configurable & Instruction-Level Extensible DSP Core," Proceedings of the ASP-DAC '98 Asian and South Pacific Design Automation Conference 1998, pages 325-326, XP-002137268

EXAMINER	*M. Nelson*	DATE CONSIDERED	6·21·05

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